# Anmol Panda

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# **EDUCATION**

### **BITS PILANI**

B.E. Hons. IN COMPUTER SCIENCE July 2016 | Goa, India Cum. GPA: 8.1/10

#### **ERASMUS EXCHANGE STUDIES**

Aug '15 - Jan '16 | Uppsala, Sweden Dept. of Information Technology, Uppsala University

### ST. FRANCIS D'ASSISI

Grad. May 2010 | Mumbai, India

## COURSEWORK

### **UNDERGRADUATE**

Real Time Systems + Practicum Microprocessor - Programming and Interfacing Graph Theory Human Computer Interaction Effective Public Speaking

# **SKILLS**

### **PROGRAMMING**

Over 5000 lines:

Java • Shell • Python • LATEX • C

Over 1000 lines:

C++ • C# • PHP • Assembly • JavaScript Familiar:

P4 • Android • MySQL • CUDA • OpenCL

### **ACHIEVMENTS**

2015 Erasmus scholarhship for exchange studies

2014 Mitacs Globalink Internship

2013 Best Persuasive Speaker, EPS (class of 60 stduents)

2009 Best Student Award, High school 2009 School Captain, High school

# COMMUNITY SERVICE

### **ABHIGYAAN**

- Taught mess workers basic and high school level mathematics for five semesters, an hour at night, twice a week.
- Organized community events like Children's Day for children of security guards and faculty, sporting events for mess workers and donation drives.

## RESEARCH

### **IIT DELHI** | Senior Project Assistant

Aug 2016 - present | New Delhi, India

- Worked under the guidance of Prof. Sorav Bansal on a project to deploy compiler optimizations in software packet processing pipelines by leveraging knowledge of the underlying hardware characteristics.
- Our compiler sources its gains majorly by (a) re-ordering and merging look-up tables, and (b) batching of packets to exploit memory level parallelism (MLP). The compiler profiles hardware characteristics such as DMA latency, size of re-order buffer, degree of MLP, and memory and L3 cache latency to determine the optimal batch size.
- I worked primarily in writing the MLP benchmarks and conducted experiments with the same and the table-join algorithm. I also wrote the chat server application, which serves as another test case for our work.

### **GPU VERIFICATION TOOLS** | UNDERGRADUATE RESEARCHER

Jan 2016 - May 2016 | BITS Pilani, Goa, India

- The thesis involved a survey of existing GPU verification tools, namely GPUVerify and GKLEE to assess their use cases, kernel bugs like data races and divergent barriers that they reported, and lastly, their usability and learn-ability aspects.
- The thesis was presented on Department day at BITS Goa and a paper based on the same was published in the IEEE conference of Parallel and Distributed Grid Computing at JUIT, Waknaghat, HP, India.

# **PROJECTS**

# MITACS GLOBALINK RESEARCH INTERNSHIP | GLOBALINK

RESEARCH INTERN

May 2015 - Aug 2015 | Prince George, BC, Canada

- Worked under the supervision of Dr. Alex Aravind along with Vignesh Muralidharan at the University of Northern British Columbia to assess the viability, accuracy and efficiency of four GPS-free de-localization algorithms to find positions of mobile bots.
- Goal of the project was to use robots to automatically seed farms.

# POSITIONS

### **CENTER FOR TECHNICAL EDUCATION** I INSTRUCTOR

Aug 2014 - May 2015 | BITS Pilani, Goa, India

• Taught OOP concepts in the first module of two courses - Application Programming in C# and Introduction to Programming in Java - in two separate semesters to a class of 20 - 22 students, along with two other instructors.

### **TEACHING** | TEACHING ASSISTANT

Aug 2013 - May 2015 | BITS Pilani, Goa, India

- Effective Public Speaking: Presented sample speeches for two different sections (120 students) and mentored students to improve oral communication skills along with one more TA.
- Microprocessors Programming and Interfacing: Prepared solutions along with three more TAs for tutorial sessions and resolved queries of students in a class of 120.
- Technical Report Writing (2 semesters): Graded quizzes, assignments and mentored students to improve written communications skills